In re the Application of: KIRA, et al. Group Art Unit: 2827

Serial No.: 08/897,953 Examiner: David E. GRAYBILL

Filed: July 24, 1997 P.T.O. Confirmation No.: 5157

For. METHOD AND SYSTEM FOR FABRICATING A SEMICONDUCTOR DEVICE

PRELIMINARY AMENDMENT

Date: August 31, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This Preliminary Amendment is being filed with a Request for Continued Examination for the above-captioned patent application. Please amend the above-captioned patent application as follows: